

REMARKS

This is intended as a full and complete response to the Office Action dated May 2, 2006, having a shortened statutory period for response set to expire on August 2, 2006. Please reconsider the claims pending in the application for reasons discussed below.

Claims 1-21 are pending in the application. Claims 1-4, 6-15 and 17-18 remain pending following entry of this response. Claims 1, 8, 9, 12 and 18 have been amended. Claims 5, 16 and 19-21 have been canceled. Applicant submits that the amendments do not introduce new matter.

Claim Objections

Claims 1-21 are objected to because of informalities. Claims 1, 8, 9 and 18 have been amended, as suggested by the Examiner, to delete the term "operative." Accordingly, Applicant submits claims 1, 8, 9 and 18, as well as their dependents, are allowable and respectfully request withdrawal of these objections.

Claim Rejections - 35 USC § 102

Claims 1-2, 5, 6, and 11 are rejected under 35 U.S.C. 102(e) as being anticipated by Taylor et al. US Pat no. 6,971,051 B2 (hereinafter, *Taylor*). Claims 1-2, 12, and 18 are rejected under 35 U.S.C. 102(e) as being anticipated by Lai et al. US Pat no. 6,671,836 B1 (hereinafter, *Lai*). Applicant respectfully traverses these rejections.

"A claim is anticipated only if each and every element as set forth in the claim is found, either expressly or inherently described, in a single prior art reference." *Verdegaal Bros. v. Union Oil Co. of California*, 814 F.2d 628, 631, 2 USPQ2d 1051, 1053 (Fed. Cir. 1987). "The identical invention must be shown in as complete detail as is contained in the ... claim." *Richardson v. Suzuki Motor Co.*, 868 F.2d 1226, 1236, 9 USPQ2d 1913, 1920 (Fed. Cir. 1989). The elements must be arranged as required by the claim. *In re Bond*, 910 F.2d 831, 15 USPQ2d 1566 (Fed. Cir. 1990).

In this case, neither *Taylor* nor *Lai* discloses “each and every element as set forth in the claims.” For example, neither *Taylor* nor *Lai* discloses utilizing a self-test control device *located within a DRAM*, to control input of the DRAM memory chip and a control input at the nonvolatile memory chip; and disconnecting control input signals from outside the multichip memory module, as recited in independent claims 1, 12 and 18.

Independent claims 1, 12 and 18 have been amended to include the limitations of claims 5, 16 and 19-21, respectively. In rejecting previous claim 5, the Examiner cited *Taylor* as disclosing disconnecting control input signals from outside the multichip memory module (Figure 3, Figure 4, column 6, lines 25-65). Applicant respectfully submits, however, that neither Figure 3 nor Figure 4 of *Taylor* depicts a self-test control device *located within a DRAM*, as claimed. In contrast, *Taylor* teaches that an *off-chip* cyclic redundancy check (CRC) module can isolate the processor during testing routines (Column 6, lines 26-35).

Accordingly, Applicant submits claims 1, 12 and 18, as well as their dependents, are allowable and respectfully request withdrawal of these rejections.

Claim Rejections - 35 USC § 103

Claims 3-4, 7-8, and 9-10 are rejected under 35 U.S.C. 103(a) as being unpatentable over *Taylor* US Pat no. 6,971,051 B2, and further in view of *Barr* US Pat no. 5,758,056.

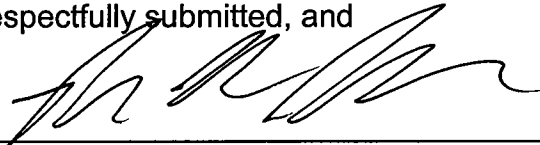
Claims 3-4, and 10 are rejected under 35 U.S.C. 103(a) as being unpatentable over *Lai* US Pat no. 6,671,836 B1, and further in view of *Barr* US Pat no. 5,758,056.

These claims depend from claim 1 which Applicant submits is allowable for reasons discussed above. Accordingly, Applicant submits these claims are also allowable and respectfully request withdrawal of this rejection.

Conclusion

Having addressed all issues set out in the office action, Applicant respectfully submits that the claims are in condition for allowance and respectfully requests that the claims be allowed.

Respectfully submitted, and



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